

**Ph.D. Viva Notification**

Mr. Ravikumar K I, Ph.D. Electronics Engineering,  
will defend his Ph.D. thesis titled

**“Design and Implementation of RRAM for ANN Based Applications”**

in an open viva to be held on

**Tuesday, 30<sup>th</sup> April 2024, 11: 00 a.m.**

**Venue: School of Engineering, JAIN Global Campus, Kanakapura Road – 562 112.**

**The External Examiners:**

Dr. U B Mahadevaswamy

Professor & Head -Department of Electronics & Communication Engineering

Sri Jayachamarajendra College of Engineering

Mysuru – 570006

Chairperson & Guide: Dr. R Sukumar,

Professor & Head – Department of ECE, SET, JAIN (Deemed-to-be University)

Date: 16<sup>th</sup> April 2024

Signed by

**Dr. Bindhu O S**

**Deputy Director**

**Centre for Researcher Training & Administration**

**JAIN (Deemed-to-be University)**