

Program Schedule

NCASMDT 2018

Day 1: Monday, 12 February 2018

08:00 am-09:00 am **Registration**

09:00am-09:15 am **Inauguration**

Session-I

Welcome Address

09:15am -09:30 am Dr. Chenraj Roychand, President, Jain University Trust,
Bengaluru

09:30am -09:45 am Dr. N. Sundararajan, Vice Chancellor, Jain University,
Bengaluru

09:45am -09:55am Dr. NVH Krishnan, Registrar, Jain University, Bengaluru

09:55am -10.05am Dr. Jayagopal Uchil, Dean of Sciences, Director, Academics
and Planning, Jain University, Bengaluru

10:05am -10:20 am Chief Guest: Dr Manoj Umesh Sharma, CEO Semiconductor
Technology and Applied Research Centre (SITAR), Bengaluru.

10:25 am -10:35 am Convenor Dr. Dinesh K Sharma

10:35am -10:50 am Tea Break

Session-II

10:50 am -11:40am **Invited Talk -1**

Speaker: Prof. S. Mohan, Emeritus Professor, Centre for
Nano Science and Engineering, Indian Institute of Science,
Bengaluru.
Theme: High K dielectric films for applications in Nano
electronic devices

11:45am -12:30 pm **Invited Talk-2**

Speaker: Prof. Navkanth Bhat, Chairperson, Centre for Nano
Science and Engineering (CeNSE), Bengaluru.
Theme: Nanotransistors with 2D materials

12:35pm -1:10 pm **Invited Talk-3**

Speaker: Dr Dinesh K Sharma, Dept. of Physics, School of
Sciences, Jain University, Bengaluru
Theme: Role of trench gate technology in semiconductor
power devices

01:10 pm-01:40pm **Lunch**

Session-III

01:40 pm -03:00 pm

Concurrent Sessions I

03:10 pm -03:50 pm

Invited Talk- 4

Speaker: Dr. Sheela Ramasesha, Principal Scientist, School of Natural Sciences and Engineering, National Institute of Advanced Studies, Indian Institute of Science Campus, Bengaluru

Theme: Nanostructured solar cell

03:50pm-04:30pm

Invited Talk -5

Speaker: Dr. C.P. Ravikumar, Director, Technical Talent Development, Texas Instrumentation, Bengaluru

Theme: Analog Design and Test: Challenges and Opportunities

04:30 pm -05:10 pm

Concurrent Sessions II

05:10 pm -05:40 pm

Sponsors Talk-1

Speaker: Prolyx Microelectronics Pvt. Ltd

05:40 pm -06:00 pm

Tea Break

06:30 pm -07:30 pm

Cultural event

07:30 pm -08:30 pm

Dinner

End of First day

Day 2: Tuesday, 13 February 2018

Session-I

09:00 am -09:50 am

Invited Talk -6

Speaker: Prof. K N Bhat, Emeritus Professor, Centre for Nano Science and Engineering, Indian Institute of Science, Bengaluru,
Theme: Hetero-junction Devices for High Speed and High Power Applications

09:50 am -10:30 am

Sponsors Talk-2

Speaker: Nano chip Solution Bengaluru

10:30-10:50

Tea Break

Session-II

10:50 am -11:30 am

Invited Talk -7

Speaker: Prof. K. S. Narayan, Professor and Dean (RandD), JNCASR, Jakkur, Bengaluru.
Theme: Device Physics of Hybrid Perovskite Structures

11:30 am -12:30 pm

Concurrent Sessions-III

12:30 pm -01:20 pm

Lunch

Session-III

01:20 pm -02:00 pm

Invited Talk -8

Speaker: Saravanan Sethuraman, IBM, Bengaluru
Theme: Emerging Memory Technologies

02:00 pm -03:00 pm

Panel Discussion

03:00 pm -03:30 pm

High Tea

03:30 pm -05:00 pm

closing ceremony