

Preparatory Exam Time Table for Electronics & Communication Engineering Department –November 2011

SI No	Date/Day	Time-Morning session	III semester	V semester
1	21/11/2011/ Monday	9.30-12.30	Mathematics-3 (MAT31)	Analog Communication EC51
2	22/11/2011/Tuesday	9.30-12.30	Analog Electronics (EC32)	Electromagnetic Theory EC52
3	23/11/2011/wednesday	9.30-12.30	Network Analysis (EC33)	Digital system Design with HDL EC53
4	24/11/2011/Thursday	9.30-12.30	Digital System Design (EC34)	Digital Signal Processing EC54
5	25/11/2011/ Friday	9.30-12.30	Signals and Systems (EC35)	Embedded system design EC55
6	26/11/2011/Saturday	9.30-12.30	Electronics Measurement (EC36)	(Elective) Digital Switching Systems EC561